

## Master Syllabus

### EET 2261 - Microprocessors

**Division:** Science, Mathematics and Engineering

**Department:** Electronics Engineering Technology

**Credit Hour Total:** 4.0

**Lecture Hrs:** 3.0 **Lab Hrs:** 3.0

**Prerequisite(s):** EET 1131

**Date Revised:** June 2014

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### Course Description:

Microprocessor architecture, assembly language programming, bus structures and timing diagrams, memory technologies and interfacing, input/output interface and systems, interrupt-processed input/output, direct memory access (DMA), microcontroller applications and microprocessor-based communications. Three classroom, three lab hours per week.

### General Education Outcomes:

- Critical Thinking/Problem Solving

### Course Outcomes:

#### Integrated Development Environment

Apply knowledge of integrated development environment machine code for different microprocessor chips.

**Assessment Method:** Locally developed exams

**Performance Criteria:** Score 70% or above

**Assessment Method:** Performance appraisals

**Performance Criteria:** Score "17.5" or above on a five by five rubric

#### Flow Charts and Program Code

Relate between the relationship of a flow chart and program code.

**Assessment Method:** Locally developed exams

**Performance Criteria:** Obtain 70% or above

**Assessment Method:** Performance appraisals

**Performance Criteria:** Score "17.5" or above on a five by five rubric

#### Technical Communication

Communicate with others about the process used in a microprocessor.

**Assessment Method:** Performance appraisals

**Performance Criteria:** Score "17.5" or above on a five by five rubric

### Outline:

Microprocessor architecture  
Assembly language programming  
Bus structures and timing diagrams  
Input/output interface and systems  
Interrupt-processed input/output  
Direct memory access  
Microcontroller applications  
Microprocessor-based communications