

Master Syllabus

EET 1198 - Digital Technology

Division: Science, Mathematics and Engineering

Department: Electronics Engineering Technology

Credit Hour Total: 2.0

Lecture Hrs: 1.0 **Lab Hrs:** 2.0

Date Revised: October 2012

Course Description:

Electrical fundamentals, introduction to basics of digital logic and circuits, digital systems and basic digital circuit design. One classroom, two lab hours per week.

General Education Outcomes:

- ▣ Critical Thinking/Problem Solving

Course Outcomes:

Number systems

Demonstrate ability to work with numerical values in different number systems.

Assessment Method: Locally developed exams

Performance Criteria: 70% of exam items correct

Logic diagram

Reconcile between logic diagram and actual circuit.

Assessment Method: Locally developed exams

Performance Criteria: 70% of exam items correct

Digital devices

Apply knowledge of digital devices to solve problems.

Assessment Method: Locally developed exams

Performance Criteria: 70% of exam items correct

Outline:

Number systems and codes
Logic functions
Binary arithmetic
Flip-flops
Registers and encoders
Decoders
Counters and timing circuits