

Master Syllabus

EET 1116 - Electronics Schematics & Fabrication

Division: Science, Mathematics and Engineering

Department: Electronics Engineering Technology

Credit Hour Total: 4.0

Lecture Hrs: 3.0 **Lab Hrs:** 3.0

Date Revised: October 2013

Course Description:

Draw circuits using Multisim. Compose directories using Windows commands. Identify schematic symbols and components. Produce a technical document with text, graphs and schematics. Assembly of circuits. Three classroom, three lab hours per week.

General Education Outcomes:

- ▣ Written Communication
- ▣ Critical Thinking/Problem Solving
- ▣ Values/Citizenship/Community
- ▣ Computer Literacy
- ▣ Information Literacy

Course Outcomes:

Electronic Drawings

Create electronic drawings using software.

Assessment Method: Locally developed exams

Performance Criteria: 70% or better

Assessment Method: Performance appraisals

Performance Criteria: 70% or better

Component and Symbol Identification

Identify electronic components and symbols.

Assessment Method: Locally developed exams

Performance Criteria: 70% or better

Windows Navigation

Use Windows software tools for file preservation and information storage.

Assessment Method: Locally developed exams

Performance Criteria: 70% or better

Technical Documents

Create a technical document with imported schematics, graphs, and text.

Assessment Method: Locally developed exams

Performance Criteria: 70% or better

Assembly

Apply assembly concepts and skills to build or repair circuits.

Assessment Method: Locally developed exams

Performance Criteria: 70% or better

Outline:

Use Multisim to draw and edit schematic drawings
Use MS Word, Excel, and Multisim to create a report
Properly scale a drawing
Introduction to LabVIEW
Identify nodes in a circuit
Assembly principles and standards
Elements of printed circuit boards
Identify components and schematic symbols